

OCT 07 2002 1:45PM

DANAMRAJ & Youst, P.C.

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P/S

#3/EX
10/16/2
Hurley

Attorney Docket No.: 1384-1023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application: John L. Pierce

Serial No.: 09/738,193

Filed: December 15, 2000

Art Unit: 2827

Examiner: David E. Graybill

For: System, Method and Apparatus for Constructing
a Semiconductor Wafer-Interposer Using B-
Stage Laminates

Commissioner of Patents
Washington, D.C. 20231

Petition Under 37 C.F.R. §1.136

Dear Sir:

Applicant hereby respectfully requests a one month extension
of time pursuant to 37 C.F.R. 1.136 for filing a reply. The

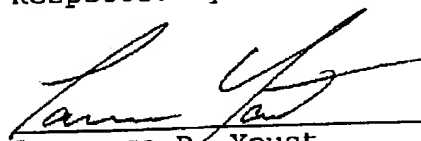
10/16/2002 SSURLES-0000002-09738193 set forth in 37 C.F.R. 1.17(a)(1) of \$55.00 is

01 FC:2251 enclosed herewith. 55:00:00

Dated this 7th day of October, 2002.

FAX COPY RECEIVED

Respectfully submitted: OCT 07 2002



Lawrence R. Youst
Reg. No. 38,795

Attorney for Applicant and Assignee

TECHNOLOGY CENTER 2800